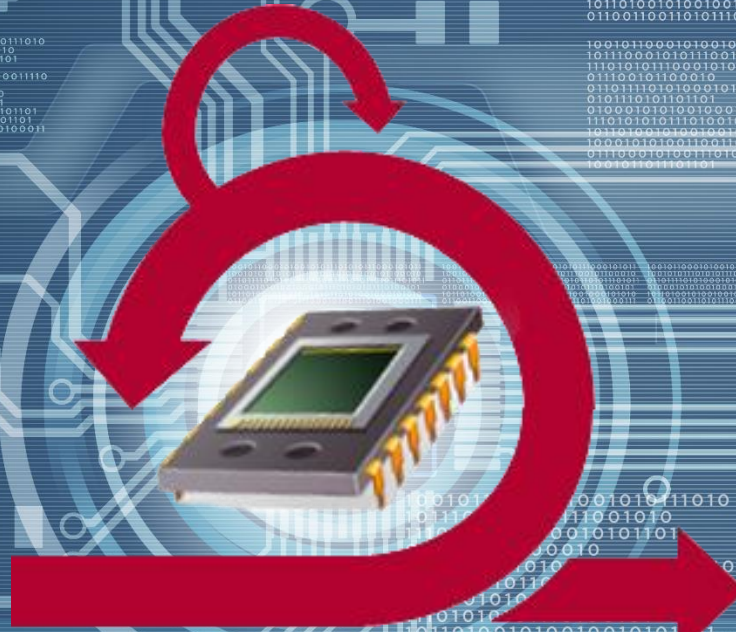


Challenges for Agile Embedded Testing



Why do differences matter for agile testing?

Dangers in Host / Target environment differences

Cross-compiler bugs

Supposedly standard library functions (printf, scanf, etc)

Correct interface operation to target input/output devices

Interaction with the RTOS or real time kernel

Ordering of bytes within words

Word length

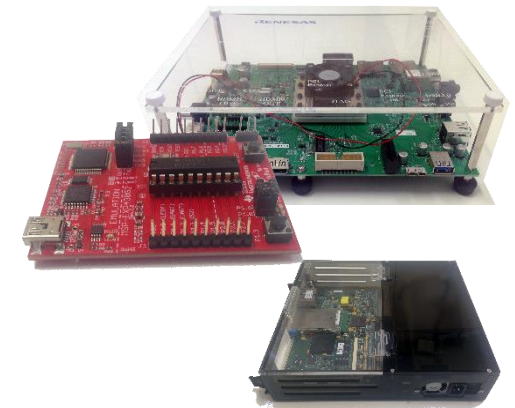
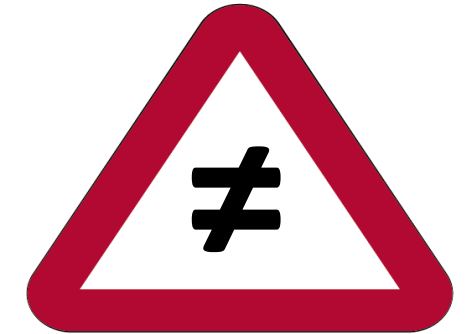
Structuring, packing of compound data (arrays, records)

Data representation

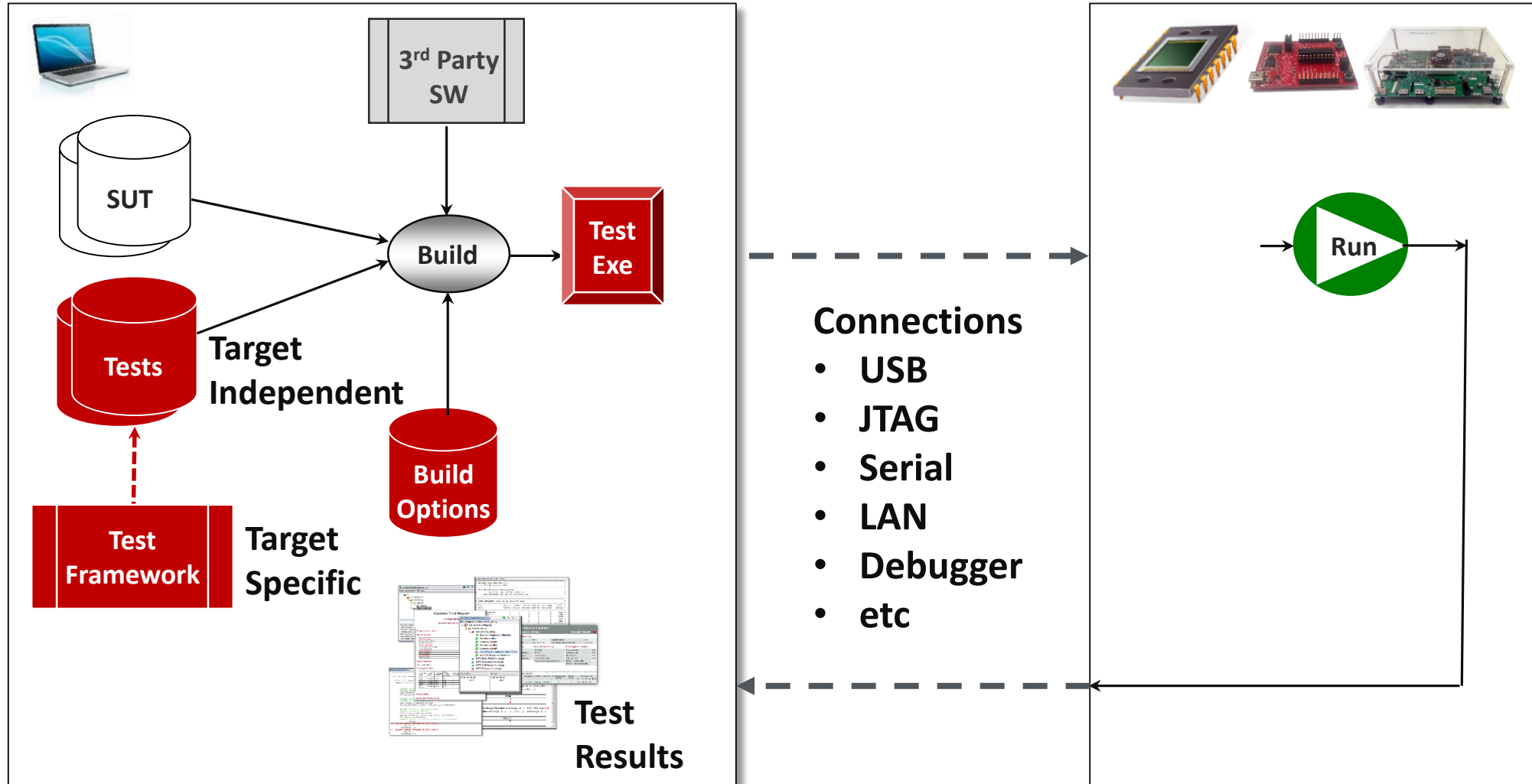
Memory constraints

Timing errors

Agile needs tests using target environment



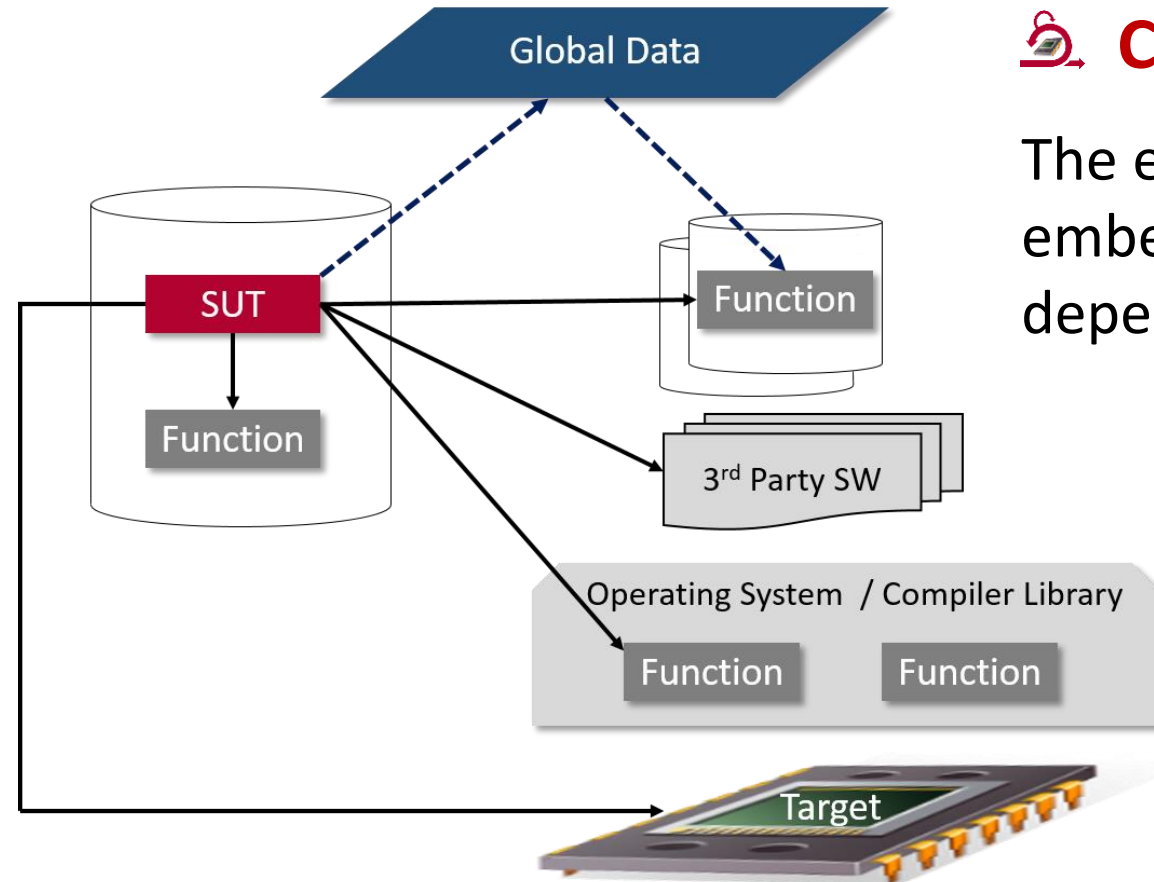
Differences can be verified by running tests on both host + target(s)



Embedded Software Under Test (SUT) dependencies

Availability






Whether an embedded environment dependency is available to be tested with.








Complexity

The extra difficulty with an embedded environment dependency in the loop.






Availability Challenges

-  **Hardware unavailable or changing**
e.g. being developed in parallel
-  **3rd Party SW is SOUP / unavailable**
-  **Limited Hardware simulation**
-  **Limited Memory on target**
-  **Kit provision costs for testing**






How can be addressed

-  Minimal Valuable Hardware, Host, then target testing
Use of Simulators
Conditional compilation (#define)
-  I/O Interception or Simulation
-  Host, then target testing
-  Supplemented memory / Tools
-  Maximise test automation on embedded platform

Complexity Challenges

-  **Embedded test set-up**
-  **Root cause analysis**
-  **Isolation from Hardware & non-SUT software**
-  **Integration with Hardware & software SW**
-  **Test automation**

How can be addressed

-  Configurable test framework
-  Limiting test scope boundary, Tests run under debugger
-  Unit testing + simulations
-  Integration tests + interceptions
-  Tools!!



Conscious Un-coupling ?



Abstract & Segregate

Abstract hardware from logic in implementation (e.g. layered software design for embedded testing)

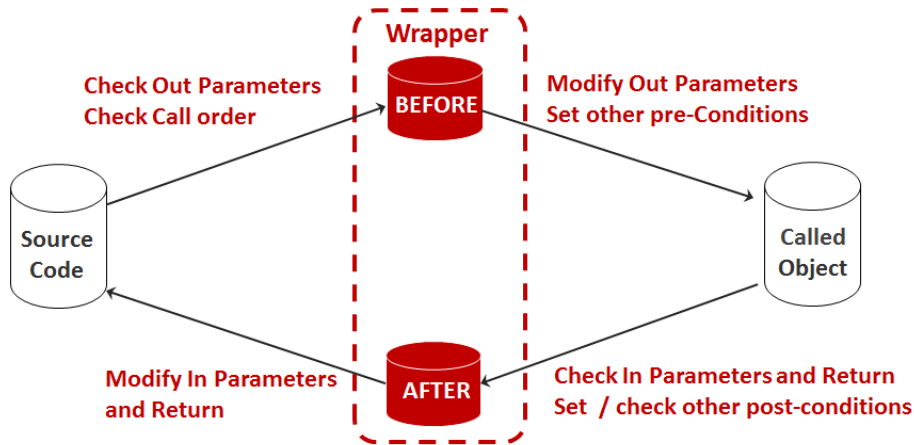
Segregate by simulating SUT interfaces to hardware / software (e.g. stub, mock, fake, etc)

Interface Interception ?

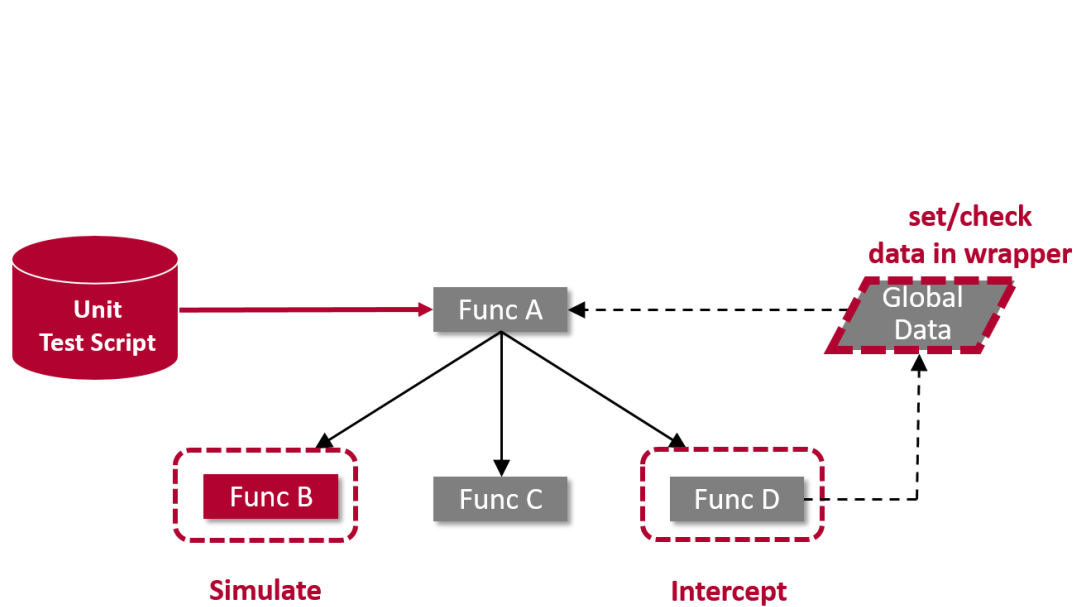
Test Integration Interfaces

Use real Hardware in Loop (HiL) testing for less 'pure' unit / integration / system tests.

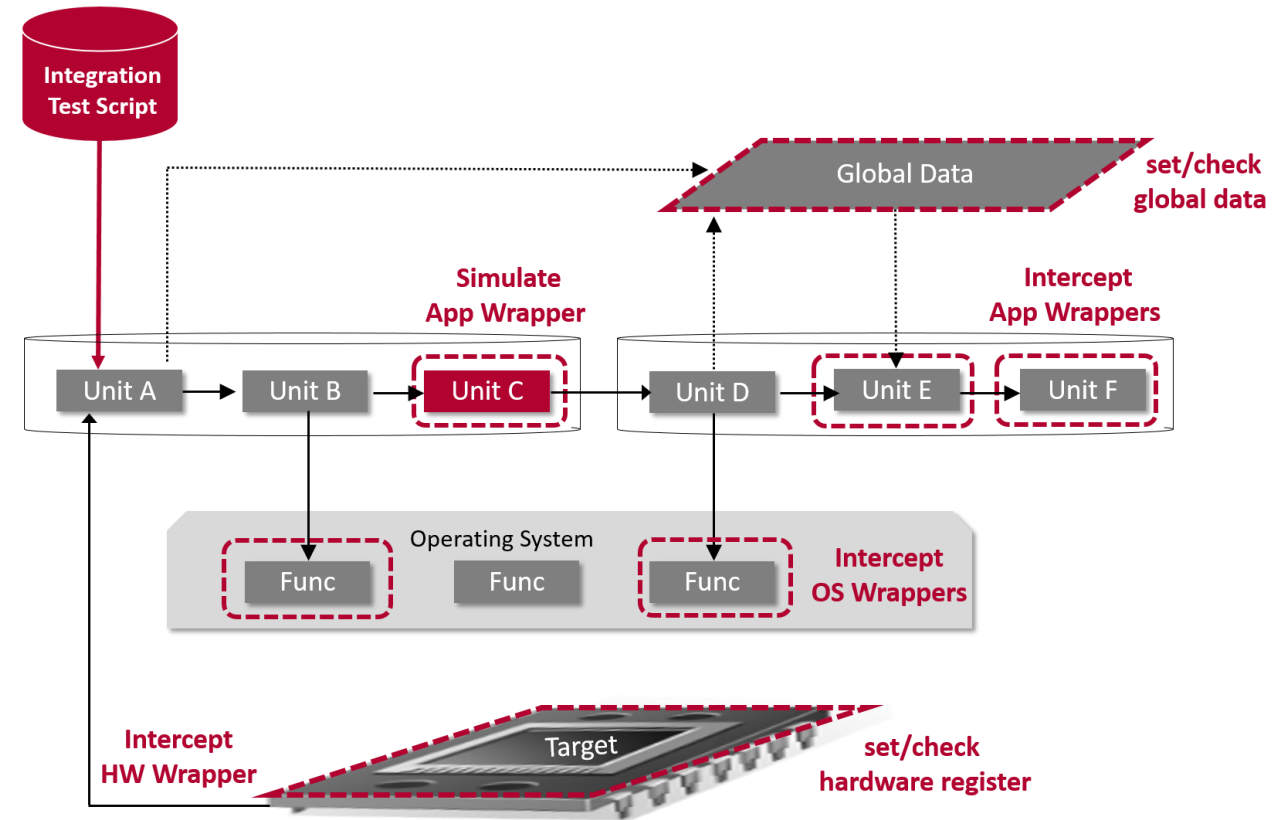
Use real code called by the SUT, but intercept calls to control them.



✓ 'Isolation' Unit Test



✓ Integration Test



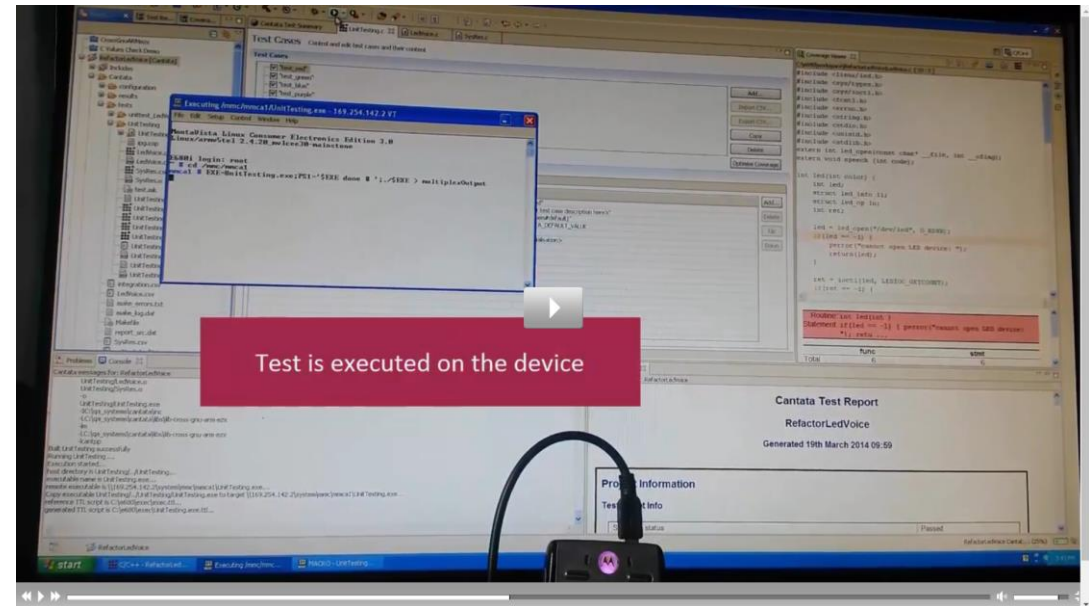
Unit Test HiL with Wrapping

Example controlling colours of an LED

Low-level calls to read / write operations on the LED port

Automatically intercepting return from LED to modify the call behavior at run time (HiL)

Injects faulty 'error conditions' back to controlling function to achieve desire code coverage



🔍 Requirements Management Tools in Agile

Suitability

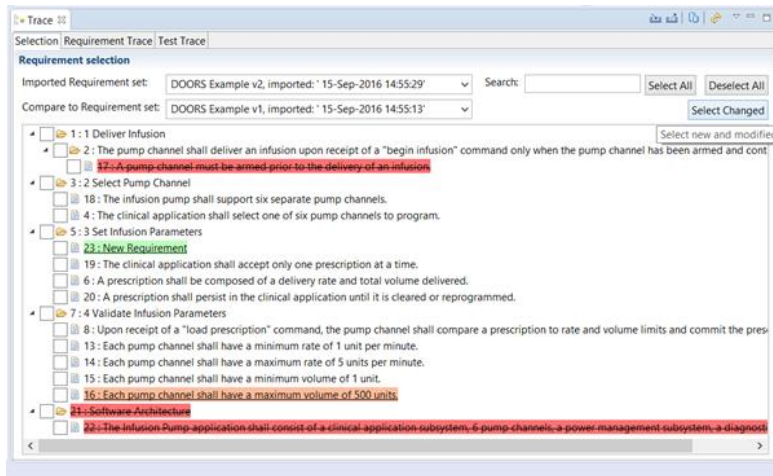
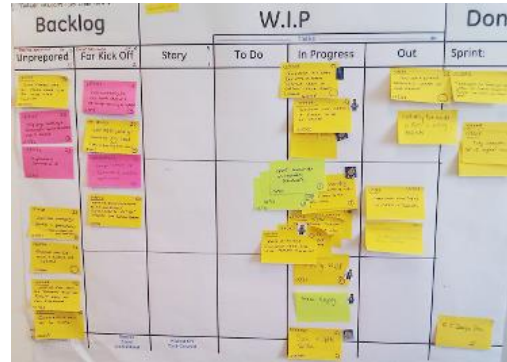
Whole team availability & use



🔍 Managing Changes

Requirements Definitions

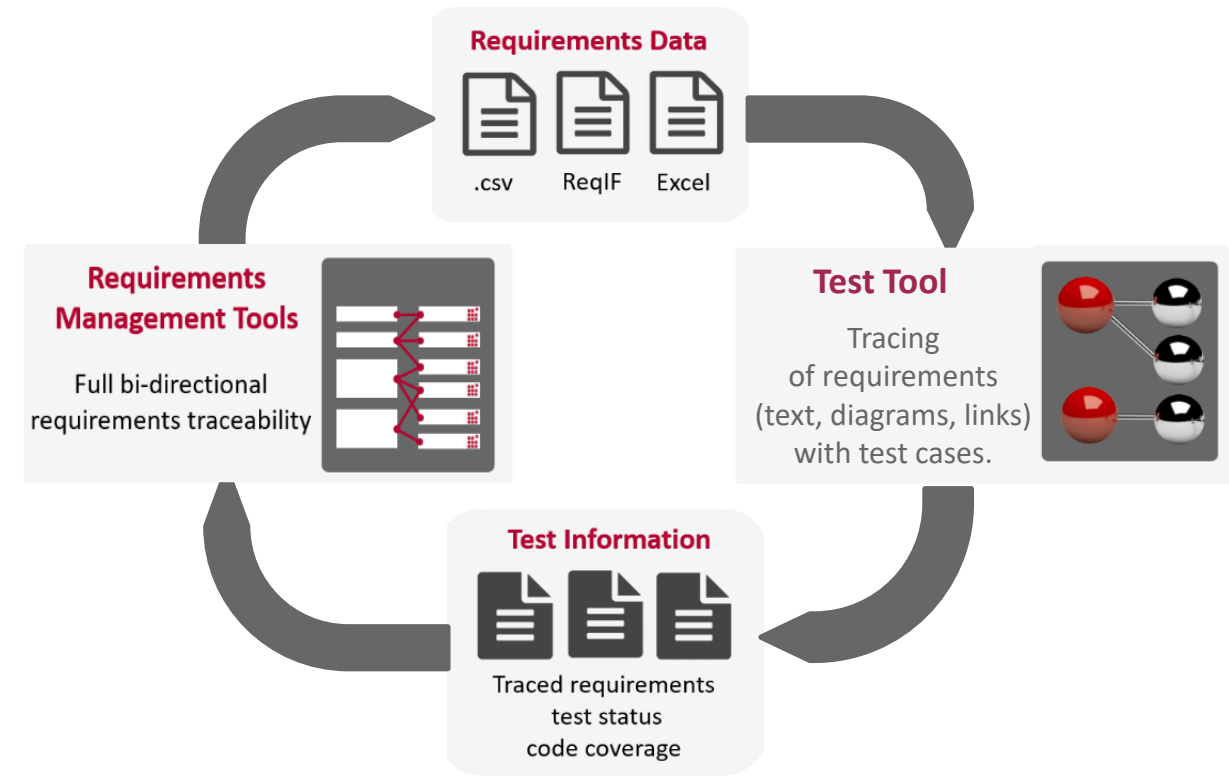
Testing work allocation



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Traceability to Tests

Visibility during testing
What to trace and when
Control of traceability data



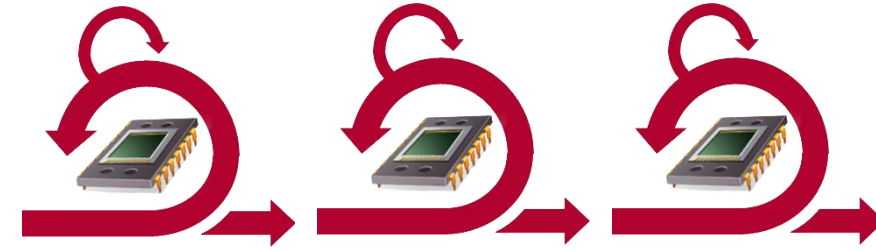
Regression Testing

Automated test suites & Continuous Testing
Incremental / Full suite test runs



Monitoring test progress

Test execution on different variants (HW & SW)



CANTATA TEAM REPORTING

Home User: admin Admin Filter Report View Help

Head up display module

Containers Summary

Additional Data Filter

Name	Value
Number of containers	4
Source files tested	13/12
Test script passes	3/7
Test case/coverage passes	105/115

Coverage

Entry Point	Statement	Decision	Call-Return
100% (7/7)	87% (69/79)	75% (21/28)	85% (6/7)

History

Filter from 08-October-2015 00:00:00

Filter to 23-February-2016 00:00:00

Test case/coverage

Coverage

Results Filtering – Additional Data

Trending history displays and Filters

Cantata Test Report

Reverse String

Generated 20th July 2015

Displaying results for: reverse_string

Test Script Info

- Function: stifen
- Instance: default
- Function: malloc
- Instance: default
- returnValue = expected_returnValue
- 100% Entry Point Coverage
- 100% Statement Coverage
- 100% Call Return Coverage

Test Results

```

PASS: Test Script: reverse_string
CALL: sequence matched so far:
system_valid#valid_data#high_value_checkeddefault
PASS: Function: low_value_checked
CALL: instance: default
system_valid#valid_data#high_value_checkeddefault
low_value_checkeddefault
PASS: Check: returnValue = expected_returnValue
value: 1

```

Test Results Summary

Category	Expected	Actual
65 GC 6F 48	0	0
010H	0	0

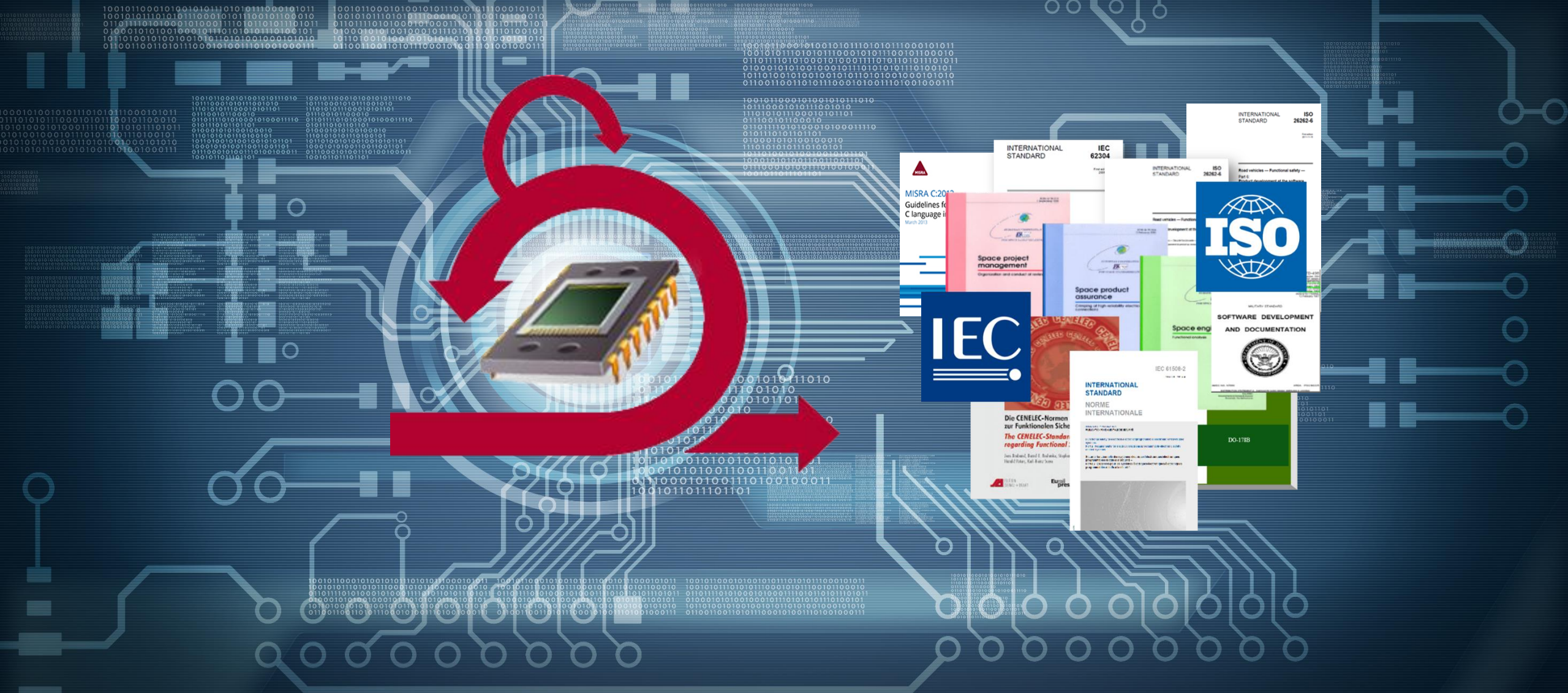
Overall Result: Fail

Summary

Item	Count	Percentage
Failed	1	100%
Passed	0	0%

Script: Masking: Operand average_b < (15.25) was not on average_a < (11.25) || average_b < (1.25) ||

How Functional Safety Impacts Agile Testing



Standards & Certifications

Industrial Specializations

Independent Certification Authorities



Standards dictate where code should be tested & by whom

As close to the running system configuration as possible

Variation by Safety Integrity Level

Role of Independent Verification & Validation

Standards dictate use of suitable test tools

The need for tool qualification / certification

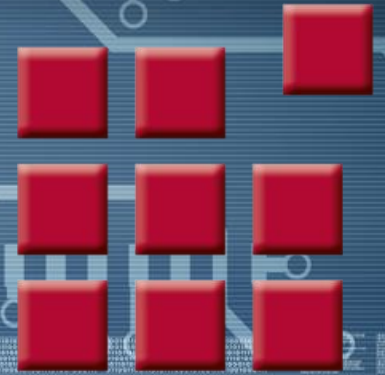
Tool configuration on embedded target test platforms

UP NEXT...

Industrial experience
with Agile in high-
integrity software
development

Altran UK

CANTATA



Certified Unit & Integration Testing

Thank you

Questions?